

REMARKS

This is in full and timely response to the non-final Office Action dated March 30, 2005 (Paper No./Mail Date 20050318). The present Amendment amends claim 1 to correct a minor matter of form and otherwise disputes certain findings of fact made in connection with the rejection of the claims. Accordingly, claims 1 to 13 are presently pending in the application, each of which is believed to be in condition for allowance. Reexamination and reconsideration in light of the present Amendment and the following remarks are respectfully requested.

Claim to Priority

Acknowledgement of the proper receipt of the certified formal papers filed in connection with Applicant's claim to priority under 35 U.S.C. § 119(a)-(d) is noted with appreciation.

Specification

The specification has been reviewed and a few minor changes made to prepare this application for final printing.

Drawings:

Acceptance of the drawings filed on April 20, 2004 is noted with appreciation.

Information Disclosure Statement

It is noted with appreciation that the PTO-1449 form that accompanied the IDS previously filed has been acknowledged.

Claim Rejections- Alleged Double Patenting

In the Action, claims 1-13 were provisionally rejected under the judicially created doctrine of obviousness-type double patenting as allegedly being unpatentable over claims 1-24 of co-pending Application No. 10/774,573 ("the '573 Application"). This rejection is respectfully traversed.

As stated in M.P.E.P § 804(II)(B)(1), "[a] double patenting rejection of the obviousness-type is 'analogous to the nonobviousness requirement of 35 U.S.C. § 103.'" (quoting *In re Braithwaite*, 379 F.2d 594, 154 USPQ 29 (CCPA 1967)). However, in order to establish a

double patenting rejection of the obviousness-type, all claim limitations must be recited within the **claims** of the cited patent. *Id.* The **disclosure** of the cited patent, however, may not be used in establishing obviousness-type double patenting. *Id.*

In the present case, the patentable line of demarcation between the claims of the present application and that of the '573 Application has been clearly drawn. For example, independent claim 1 of the present application recites "a semiconductor memory apparatus ... wherein ... said redundant lines are shared by said unit blocks pertaining to said **two-dimensional group**", whereas independent claims 1 and 6 of the '573 Application fail to recite this limitation. In addition, independent claim 6 recites "A self-repair method ... wherein ... said unit blocks are further laid out to form a block matrix or a plurality of block matrixes, and every plurality of said unit blocks forms a **two-dimensional** group oriented in a first direction (row or column direction) and a second direction (column or row direction)" whereas independent claims 17 and 20 of the '572 Application fail to recite this limitation.

Thus, as has been clearly demonstrated above, the claims of the present invention (and in particular each independent claim of the present application) have a number of clearly patentable differences from claims 1-24 of the '573 Application. Accordingly, withdrawal of this rejection is courteously solicited.

Claims 1-13 were also provisionally rejected under the judicially created doctrine of obviousness-type double patenting as allegedly being unpatentable over claims 1-18 of co-pending Application No. 10/823,572 ("the '572 Application"). This rejection is respectfully traversed.

As with the '573 Application, the patentable line of demarcation between the claims of the present application and that of the '572 Application has been clearly drawn. For example, independent claim 1 of the present application recites "a semiconductor memory apparatus ... wherein ... said redundant lines are shared by said unit blocks pertaining to said **two-dimensional group**", whereas independent claim 1 of the '572 Application recites "a semiconductor memory apparatus ... wherein ... said redundant lines are shared by said unit blocks pertaining to said **one-dimensional group**". In addition, independent claim 6 recites "A self-repair method ... wherein ... said unit blocks are further laid out to form a block matrix or a plurality of block matrixes, and every plurality of said unit blocks forms a **two-dimensional** group oriented in a first direction (row or column direction) and a second direction (column or row direction)", whereas independent claim 15 of the '572 Application recites "a **one-**

dimensional group”.

Thus, as has been clearly demonstrated above, the claims of the present invention (and in particular each independent claim of the present application) have a number of clearly patentable differences from claims 1-18 of the ‘572 Application. Accordingly, withdrawal of this rejection is courteously solicited.

Terminal Disclaimer

Although the Applicant believes the claims of the present application are clearly patentably distinguishable from those of the ‘351 Application and the ‘573 Application, in order to expedite prosecution the Applicant is prepared to proffer a terminal disclaimer prepared in accordance with 37 C.F.R. § 1.321(c). Should the examiner desire the filing of such a terminal disclaimer, the examiner is invited to contact the Applicant’s representative at the telephone number or address provided below.

Conclusion

For at least the foregoing reasons, each of the presently pending claims in this application is believed to be in immediate condition for allowance. Accordingly, the examiner is respectfully requested to pass this application to issue. If the examiner has any comments or suggestions that could place this application in even better form, the examiner is invited to telephone the undersigned attorney at the below-listed number.

Applicant believes no fee is due with this response. However, if a fee is due, please charge our Deposit Account No. 18-0013, under Order No. SON-2999, from which the undersigned is authorized to draw.

Dated: May 13, 2005

Respectfully submitted,

By 

Ronald P. Kananen

Registration No.: 24,104

RADER, FISHMAN & GRAUER PLLC

1233 20th Street, N.W.

Suite 501

Washington, DC 20036

(202) 955-3750

Attorney for Applicant